

FIG.1

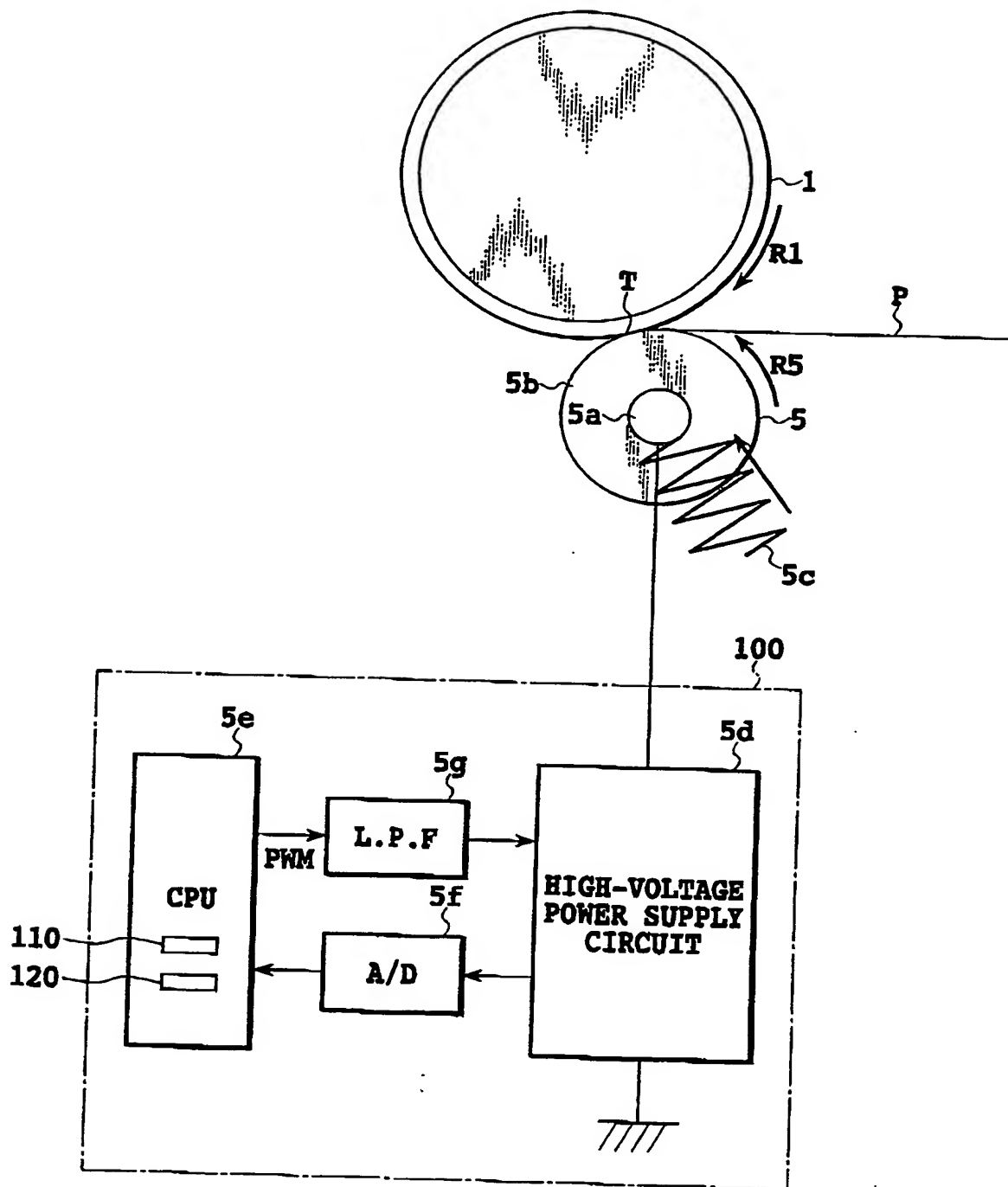


FIG.2

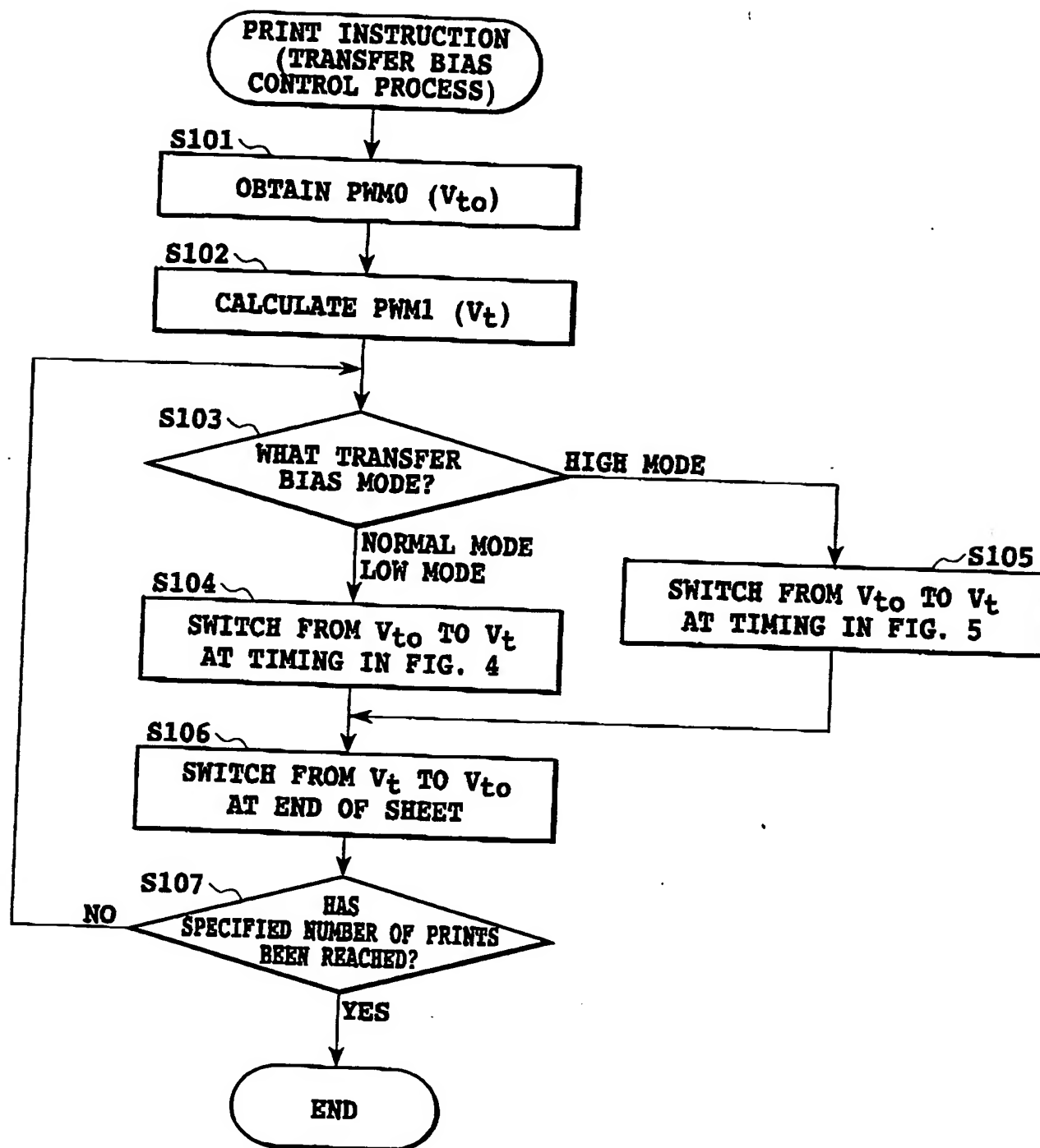


FIG.3

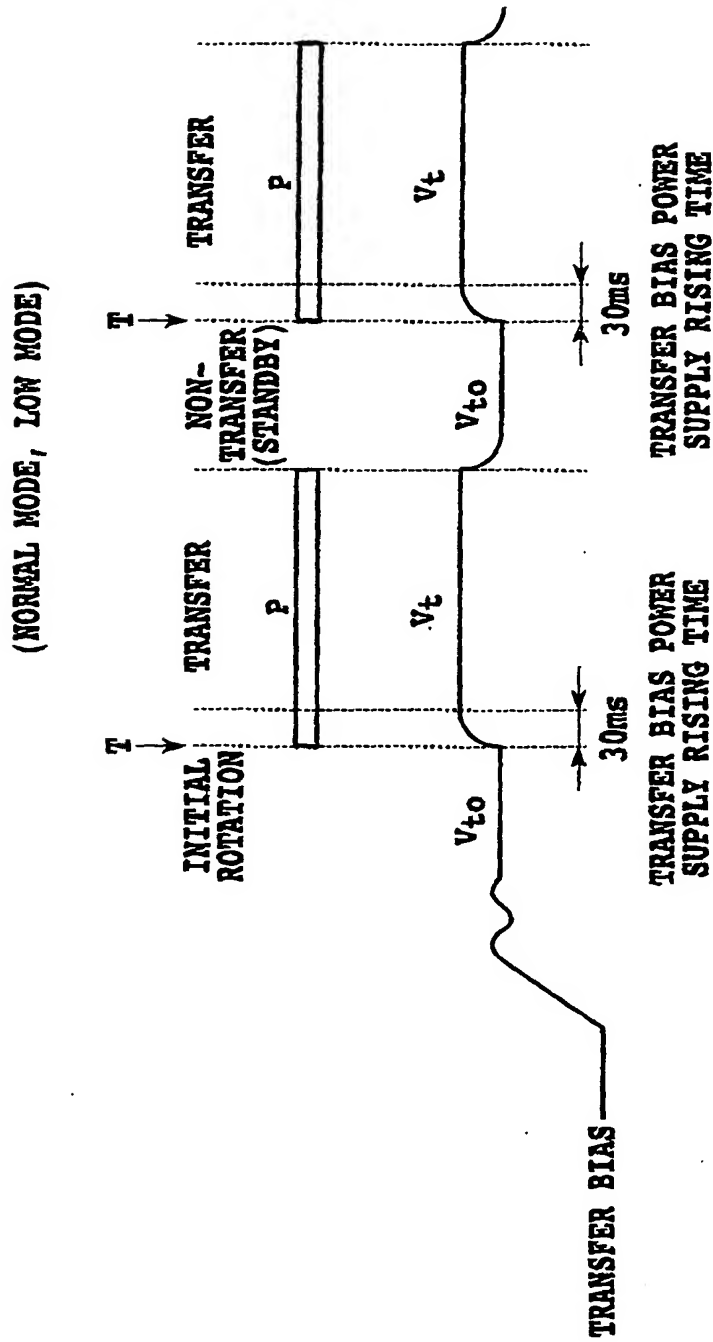


FIG.4

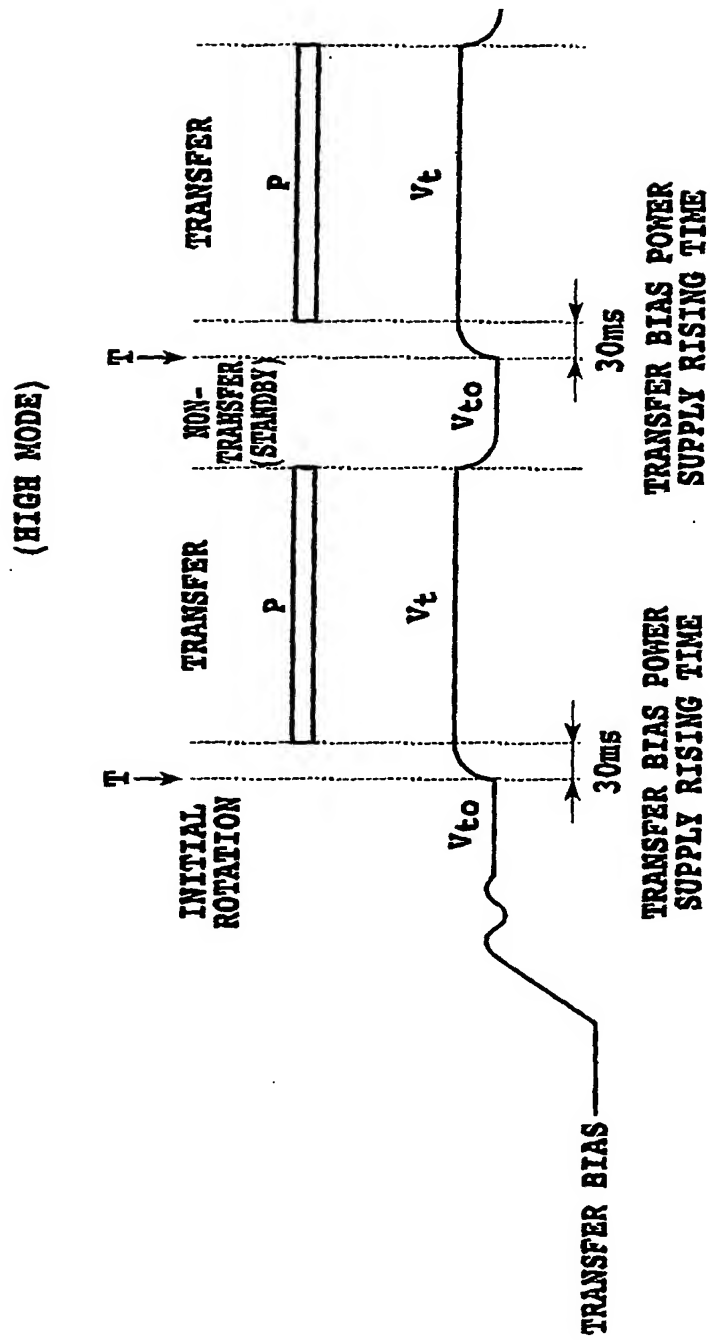


FIG.5

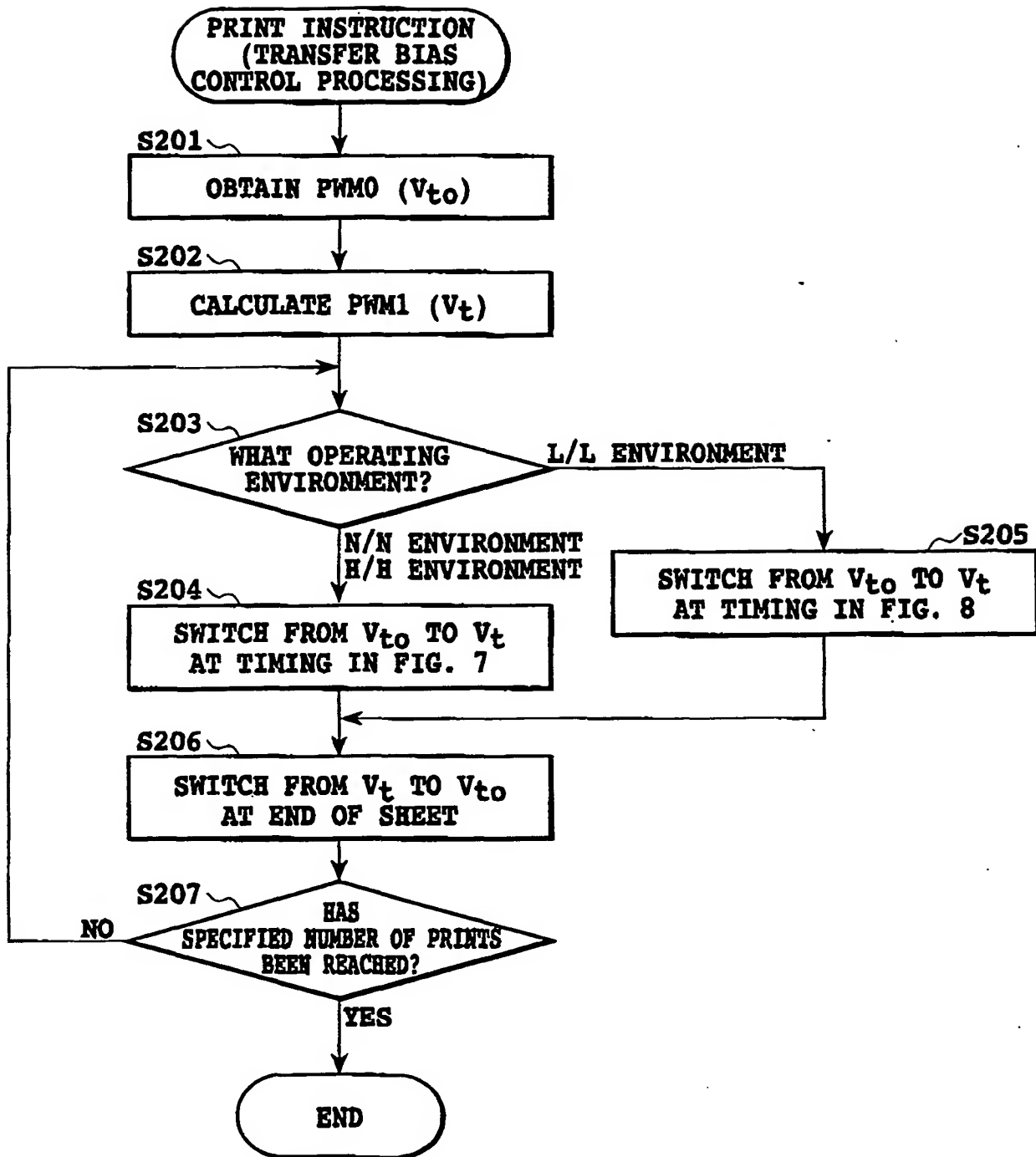


FIG.6

(H/H ENVIRONMENT, N/N ENVIRONMENT)

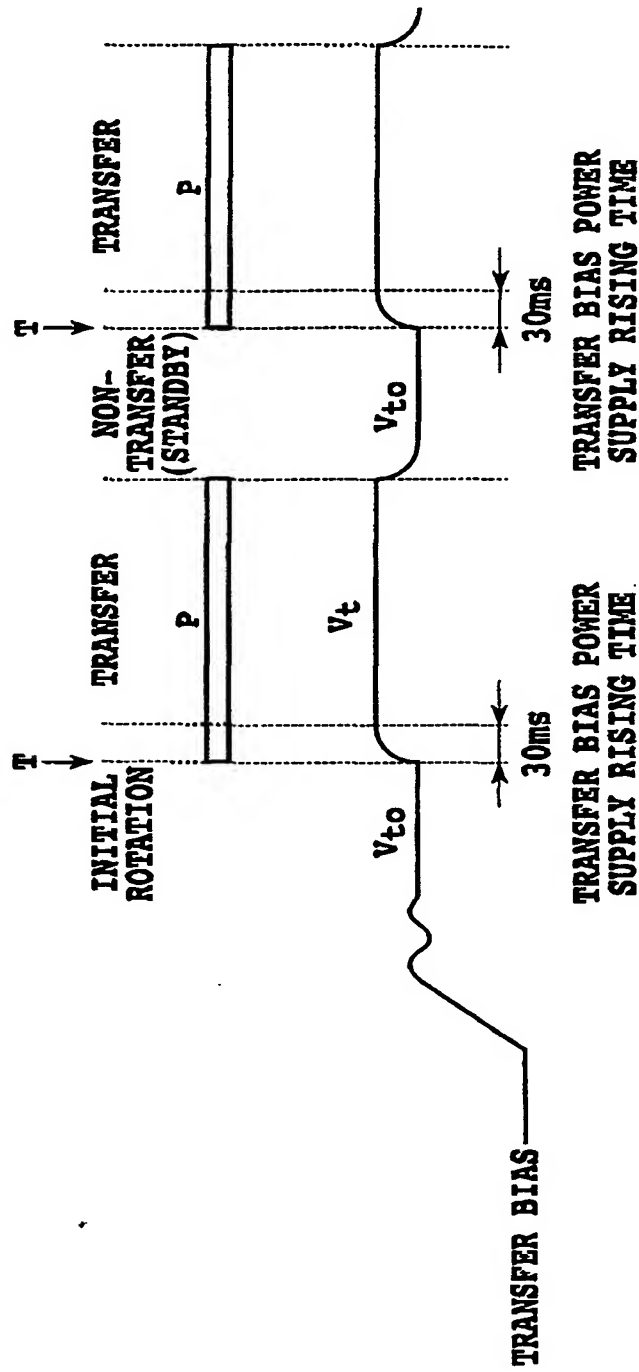


FIG.7

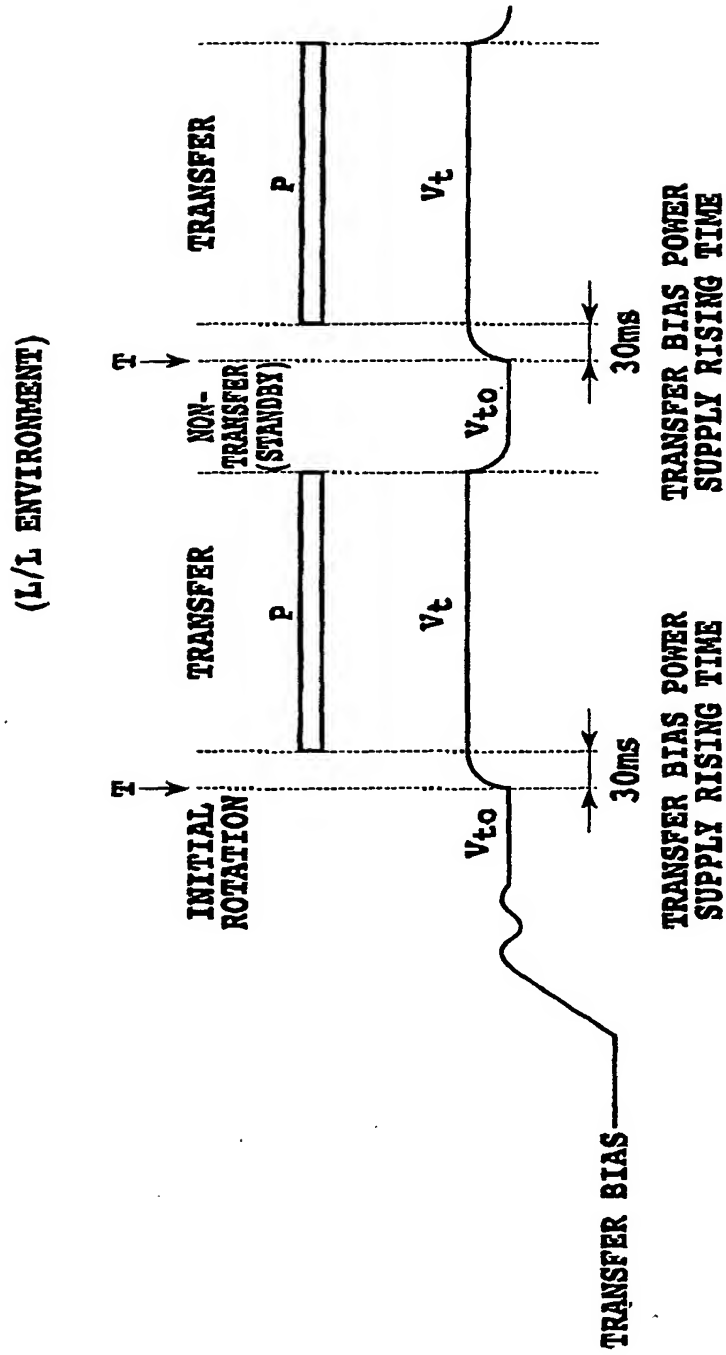


FIG.8

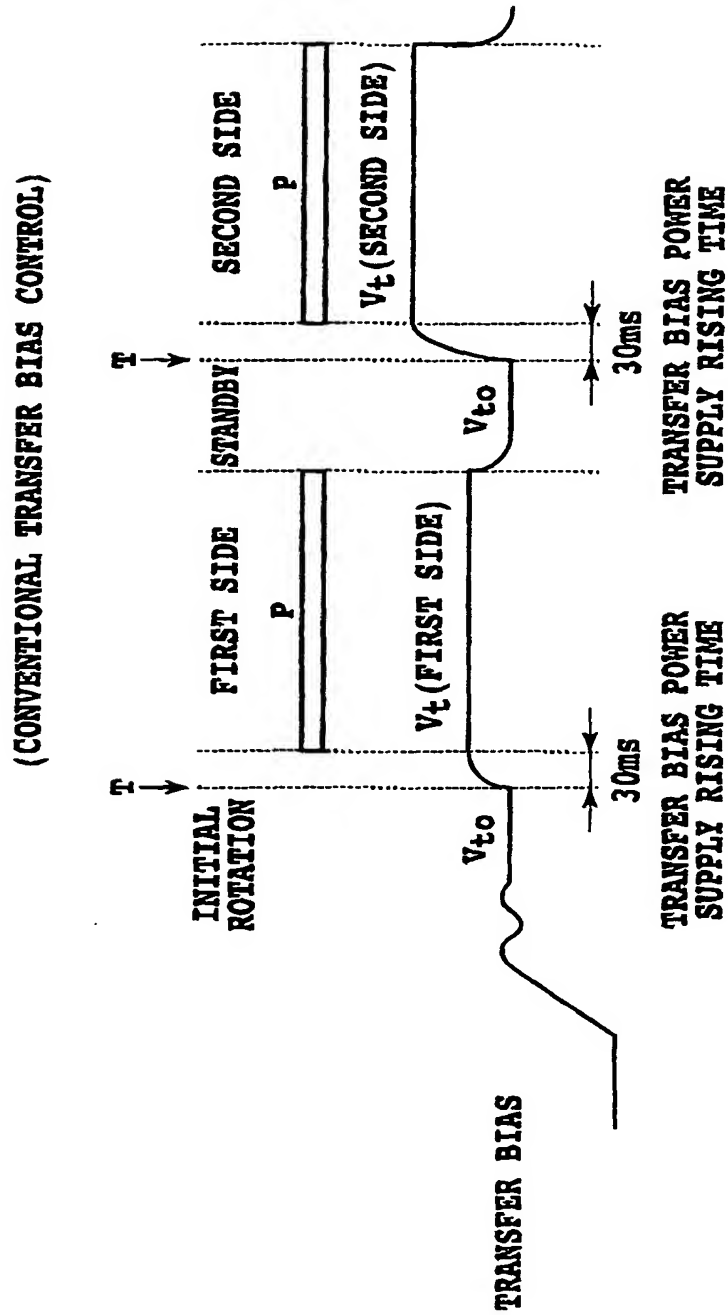


FIG.9

(TRANSFER BIAS CONTROL BY PRESENT INVENTION)

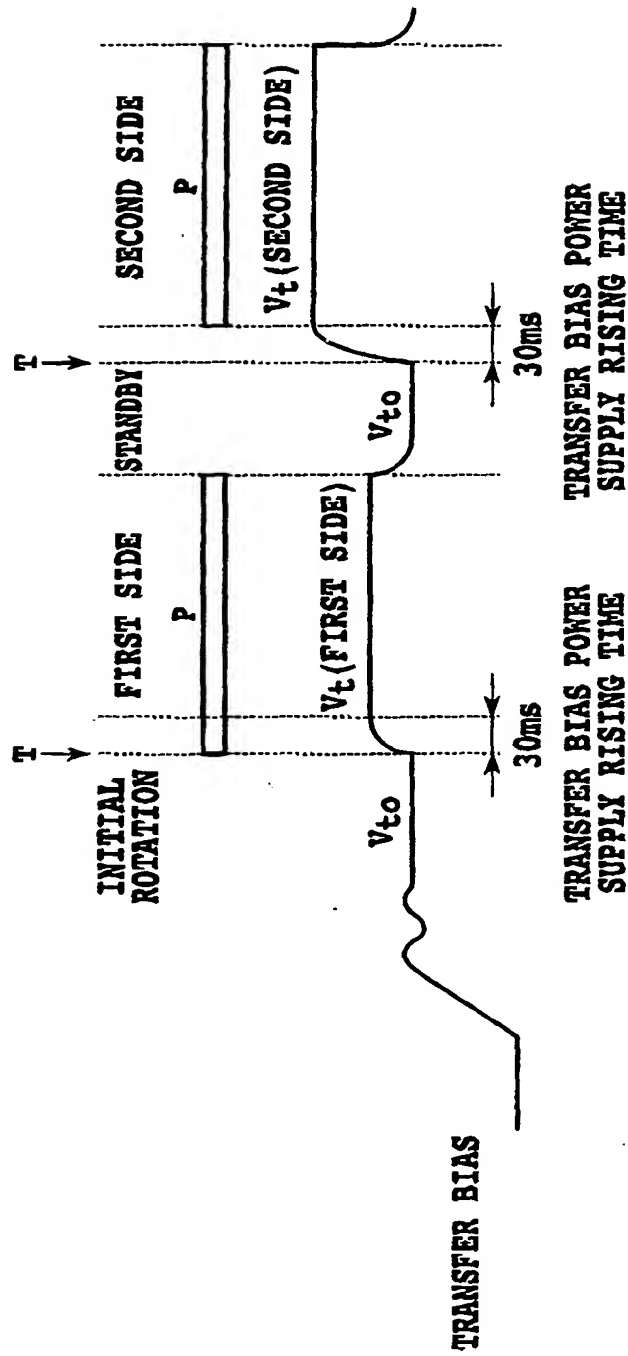


FIG.10

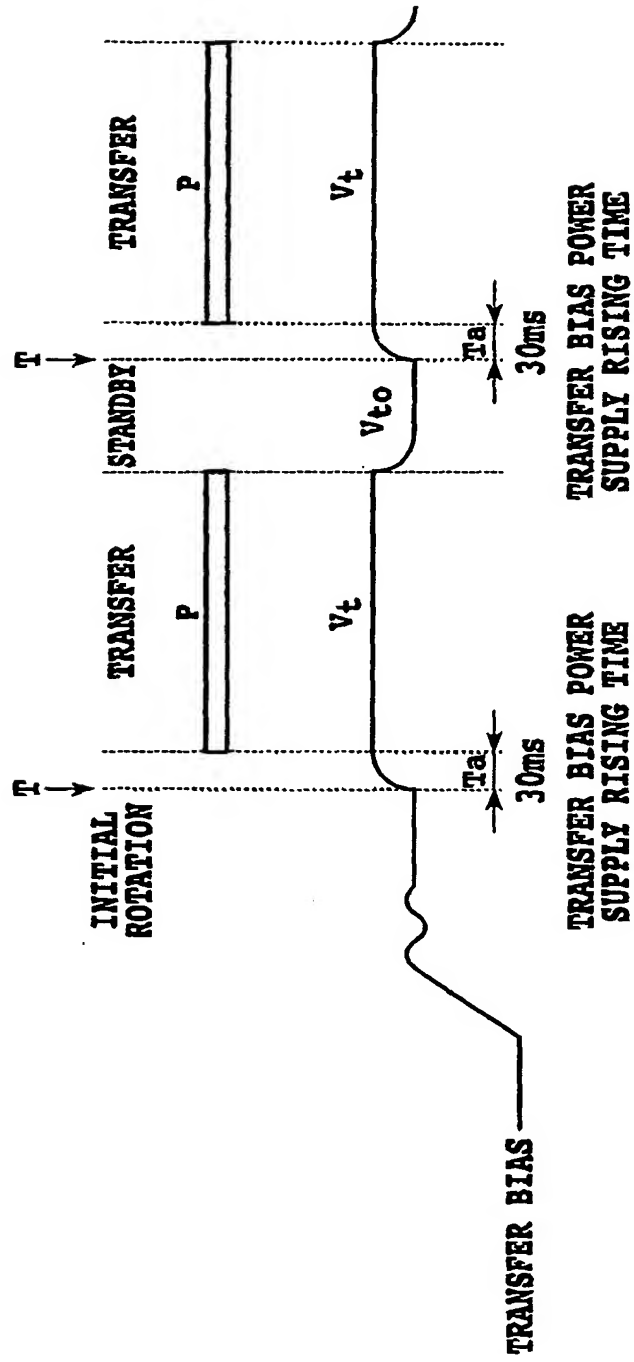


FIG.11